## Digital transistors (built-in resistors) DTB123EK / DTB123ES

## -Features

1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
3) Only the on / off conditions need to be set for operation, making device design easy.

## - Structure

PNP digital transistor
(Built-in resistor type)

- External dimensions (Unit : mm)

- Absolute maximum ratings $\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| Parameter | Symbol | Limits(DTB123ED) |  | Unit |
| :---: | :---: | :---: | :---: | :---: |
|  |  | K | S |  |
| Supply voltage | Vcc | -50 |  | V |
| Input voltage | Vin | -12 |  | V |
|  |  | 10 |  |  |
| Output current | Ic | -500 |  | mA |
| Power dissipation | Pd | 200 | 300 | mW |
| Junction temperature | Tj | 150 |  | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | Tstg | -55 to +150 |  | ${ }^{\circ} \mathrm{C}$ |

- Equivalent circuit


Transistors
-Electrical characteristics $\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input voltage | $\mathrm{V}_{\text {I(off) }}$ | - | - | -0.5 | V | $\mathrm{Vcc}=-5 \mathrm{~V}$, $\mathrm{lo}=-100 \mu \mathrm{~A}$ |
|  | $\mathrm{V}_{\text {I(on) }}$ | -3 | - | - |  | V o $=-0.3 \mathrm{~V}$, $\mathrm{lo}=-20 \mathrm{~mA}$ |
| Output voltage | Vo(on) | - | -0.1 | -0.3 | V | $\mathrm{lo} / \mathrm{l}=-50 \mathrm{~mA} /-2.5 \mathrm{~mA}$ |
| Input current | 11 | - | - | -3.8 | mA | $\mathrm{V}_{\mathrm{l}}=-5 \mathrm{~V}$ |
| Output current | lo(off) | - | - | -0.5 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{cc}}=-50 \mathrm{~V}, \mathrm{~V}_{1}=0 \mathrm{~V}$ |
| DC current gain | GI | 39 | - |  | - | $\mathrm{Vo}=-5 \mathrm{~V}$, $\mathrm{lo}=-50 \mathrm{~mA}$ |
| Input resistance | $\mathrm{R}_{1}$ | 1.54 | 2.2 | 2.86 | $\mathrm{k} \Omega$ | - |
| Resistance ratio | $\mathrm{R}_{2} / \mathrm{R}_{1}$ | 0.8 | 1 | 1.2 | - | - |
| Transition frequency | $\mathrm{ft}^{\text {}}$ | - | 200 | - | MHz | VCE $=-10 \mathrm{~V}, \mathrm{IE}=50 \mathrm{~mA}, \mathrm{f}=100 \mathrm{MHz}$ |

* Transition frequency of the device


## $\bullet$-Packaging specifications

|  | Package | SMT3 | SPT |
| :--- | :--- | :---: | :---: |
|  | Packaging type | Taping | Taping |
|  | Code | T146 | TP |
|  | Basic ordering <br> unit (pieces) | 3000 | 5000 |
| DTB123EK | $\bigcirc$ | - |  |
| DTB123ES | - | $\bigcirc$ |  |

## - Electrical characteristic curves



OUTPUT CURRENT : lo (A)
Fig. 1 Input voltage vs. output current (ON characteristics)


OUTPUT CURRENT : lo (A)
Fig. 4 Output voltage vs. output current


INPUT VOLTAGE : $\mathrm{V}_{\text {(off) }}(\mathrm{V})$
Fig. 2 Output current vs. input voltage (OFF characteristics)


Fig. 3 DC current gain vs. output current

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